

1.4 Final Exam

Thursday, April 11th, 02:30PM - 04:30 PM (Location TBA).

Exam time and location is subject to change. Please see WebAdvisor for the latest information.

2 Instructional Support

2.1 Instructional Support Team

Instructor: Mohamed Hassan Ph.D.
Email: mohassan@uoguelph.ca
Office: THRN 2407
Office Hours: By appointment

Lab Technician: Matthew Saunders
Email: msaund05@uoguelph.ca
Telephone: 519-824-4120 ext: 53916
Office: THRN 1506

2.2 Teaching Assistant(s)

Teaching Assistant: Umaira Shaikh
Email: ushaikh@uoguelph.ca

3 Learning Resources

3.1 Required Resource(s)

Course Website (Website)

<https://courselink.uoguelph.ca>

Course material, news, announcements, and grades will be regularly posted to the ENGG*3380 Courselink site. You are responsible for checking the site regularly.

Computer Organization and Design: The hardware/software interface (Textbook)

A. Patterson and J.L. Hennessy, 5th Edition, Morgan Kaufmann, 2014.

3.2 Recommended Resource(s)

VHDL for Engineers (Textbook)

Short, 2nd Edition, Prentice Hall, 2008.

Computer Organization and Architecture (Textbook)

Stallings, 10th Edition, Pearson.

Computer Systems Organization and Architecture (Textbook)

Carpinelli, Addison Wesley.

Vranesic Computer Organization and Embedded Systems (Textbook)

Hamacher, Z., 6th Edition, McGraw Hill.

3.3 Additional Resource(s)**Lecture Information (Notes)**

All the lecture notes are posted on CourseLink

Lab Information (Lab Manual)

The handouts for all the lab sessions will be posted to CourseLink

Assignments (Notes)

assignments (if exist) will be posted to CourseLink and will be announced in lectures.

Exams (Other)

Some midterms and finals of previous years are posted as samples of exams. The solutions are also posted for your convenience.

Miscellaneous Information (Other)

Other information related to Computer Organization are also posted on the webpage.

4 Learning Outcomes**4.1 Course Learning Outcomes**

By the end of this course, you should be able to:

1. Thoroughly understand computer organization and the impact of various components on performance
2. Evaluate computer performance
3. Design an instruction set to target a particular class of applications
4. Design a microprocessor including datapath and control unit
5. Implement a microprocessor design and measure its performance
6. Thoroughly understand microprocessor pipeline design and analyze its performance.
7. Understand memory hierarchy and design tradeoffs.
8. Thorough understanding of VHDL and how it can be used to implement digital designs

4.2 Engineers Canada - Graduate Attributes (2018)

Successfully completing this course will contribute to the following:

#	Outcome	Learning Outcome(s)
1	Knowledge Base	8
1.4	Recall, describe and apply program-specific engineering principles and concepts	8
2	Problem Analysis	4
2.1	Formulate a problem statement in engineering and non-engineering terminology	4
2.2	Identify, organize and justify appropriate information, including assumptions	4
2.3	Construct a conceptual framework and select an appropriate solution approach	4
2.4	Execute an engineering solution	4
2.5	Critique and appraise solution approach and results	4
3	Investigation	4
3.2	Design and apply an experimental plan/investigative approach (for example, to characterize, test or troubleshoot a system)	4
3.3	Analyze and interpret experimental data	4
4	Design	4, 7
4.1	Describe design process used to develop design solution	4
4.2	Construct design-specific problem statements including the definition of criteria and constraints	4, 7
4.3	Create a variety of engineering design solutions	4, 7
4.4	Evaluate alternative design solutions based on problem definition	4, 7
4.5	Develop and refine an engineering design solution, through techniques such as iteration, simulation and/or prototyping	4, 7
5	Use of Engineering Tools	4, 7, 8
5.1	Select appropriate engineering tools from various alternatives	4, 7
5.2	Demonstrate proficiency in the application of selected engineering tools	4, 7, 8
5.3	Recognize limitations of selected engineering tools	4, 7, 8

#	Outcome	Learning Outcome(s)
6	Individual & Teamwork	4, 7
6.1	Describe principles of team dynamics and leadership	4
6.2	Understand all members' roles and responsibilities within a team	4
6.3	Execute and adapt individual role to promote team success through, for example, timeliness, respect, positive attitude	4
6.4	Apply strategies to mitigate and/or resolve conflicts	4, 7
6.5	Demonstrate leadership through, for example, influencing team vision and process, promoting a positive team culture, and inspiring team members to excel	4, 7
7	Communication Skills	7
7.2	Interpret technical documentation such as device specification sheets, drawings, diagrams, flowcharts, and pseudocode	7
7.3	Construct the finished elements using accepted norms in English, graphical standards, and engineering conventions, as appropriate for the message and audience	7

4.3 Relationships with other Courses

Previous Courses:

ENGG*2410: Principles of digital computing systems, introduction to VHDL

Follow-up Courses:

ENGG*3640 Microcomputer Interfacing: Interfacing computer to external equipment and data acquisition systems.

ENGG*4540 Advanced Computer Architecture: extend the architecture topics covered in ENGG 3380 to more advanced areas like instruction level parallelism and multi-core processors.

5 Teaching and Learning Activities

5.1 Lecture

Topic(s):	Instruction Set Architecture (2-3 Weeks)
Reference(s):	Patterson(Ch.2) , Hamacher (Ch.2)
Topic(s):	Performance Issues (~1 Week)
Reference(s):	Patterson (Ch.1)
Topic(s):	CPU Design, Control (MicroProg) (~2Weeks)
Reference(s):	Patterson(D-2), Hamacher (Ch.9), Stallings Ch.21), Carpinelli (Ch.7)
Topic(s):	CPU Design, Data Path (~ 2 Weeks)
Reference(s):	Patterson (Ch.4), Carpinelli (Ch.6)
Topic(s):	Arithmetic for Computers (~1-2 Weeks)
Reference(s):	Patterson (Ch.3), Hamacher (Ch.9)
Topic(s):	Pipelining (~2 Weeks)
Reference(s):	Patterson (Ch.4), Hamacher (Ch. 6)
Topic(s):	Memory hierarchy (Cache) (~ 2 Weeks)
Reference(s):	Patterson (Ch.5), Stallings (Ch.4)
Topic(s):	Review

5.2 Lab

Topic(s): There are 6 labs in this course that are designed to cover VHDL and the FPGA board used to implement the project design. Additional information regarding the labs will be posted on the web page.

L1: Time Multiplexed 7-Segment Display

Demo: Yes
Report: None

Due: Week 2

Topic(s): L2: Implementing Register Files & Memory in VHDL
 Demo: Yes
 Report: Yes
 Due: Week 3

Topic(s): L3: ALU Design
 Demo: Yes
 Report: Yes
 Due: Week 4

Topic(s): L4: Buses and Peripheral Devices
 Demo: Yes
 Report: Yes
 Due: Week 5

Topic(s): L5: Micro Programmed Control Units
 Demo: Yes
 Report: Yes
 Due: Week 6

Topic(s): L6: Integration: ALU + RegFile + Memory + Control Unit
 Demo: Yes
 Report: Yes
 Due: Week 7

6 Assessments

6.1 Assessment Details

Assignment (5%)

Learning Outcome(s): 1,2,6,7

Labs (20%)

Date: See Section 5 for due dates

Learning Outcome(s): 1,2,3,4,5,6,7,8

Project (25%)

Learning Outcome(s): 1,2,3,4,5,6,7,8

Midterm Exam (15%)

Date: 8, Lectures

Learning Outcome(s): 1,2,6,7

Midterm will be in Lecture time during Week 8. Details or modifications will be given in lectures.

Final Exam (35%)

Date: Thu, Apr 11, 2:30 PM - 4:30 PM, (Location TBA)

Learning Outcome(s): 1,2,6,7

7 Course Statements

7.1 Course Grading Policies

Missed Assessments: If you are unable to meet an in-course requirement due to medical, psychological, or compassionate reasons, please email the course instructor. See the undergraduate calendar for information on regulations and procedures for Academic Consideration: <http://www.uoguelph.ca/registrar/calendars/undergraduate/current/c08/c08-ac.shtml>

Accommodation of Religious Obligations: If you are unable to meet an in-course requirement due to religious obligations, please email the course instructor within two weeks of the start of the semester to make alternate arrangements. See the undergraduate calendar for information on regulations and procedures for Academic Consideration of Religious Obligations: <http://www.uoguelph.ca/registrar/calendars/undergraduate/current/c08/c08-accomrelig.shtml>

Passing grade: In order to pass the course, you must pass both the laboratory and exam portions. Students must obtain a grade of 50% or higher on the exam portion of the course in order for the laboratory write-up portion of the course to count towards the final grade.

Missed midterm: If you miss a test due to grounds for granting academic consideration or religious accommodation, the weight of any missed test will be added to the final exam weight. There will be **no makeup midterm tests**.

Lab Work: You must attend and complete all laboratories. If you miss a laboratory due to grounds for granting academic consideration or religious accommodation, arrangements must be made with the teaching assistant to complete a makeup lab.

Late Lab Reports: Late submissions of lab reports will be subject to the following penalty policy.

- 25% will be deducted if the report is up to 24 hours late,
- 50% will be deducted if the report is 24 to 48 hours late,
- No reports will be accepted after 48 hours of the due date.

8 School of Engineering Statements

8.1 Instructor's Role and Responsibility to Students

The instructor's role is to develop and deliver course material in ways that facilitate learning

for a variety of students. Selected lecture notes will be made available to students on Courselink but these are not intended to be stand-alone course notes. Some written lecture notes will be presented only in class. During lectures, the instructor will expand and explain the content of notes and provide example problems that supplement posted notes. Scheduled classes will be the principal venue to provide information and feedback for tests and labs.

8.2 Students' Learning Responsibilities

Students are expected to take advantage of the learning opportunities provided during lectures and lab sessions. Students, especially those having difficulty with the course content, should also make use of other resources recommended by the instructor. Students who do (or may) fall behind due to illness, work, or extra-curricular activities are advised to keep the instructor informed. This will allow the instructor to recommend extra resources in a timely manner and/or provide consideration if appropriate.

8.3 Lab Safety

Safety is critically important to the School and is the responsibility of all members of the School: faculty, staff and students. As a student in a lab course you are responsible for taking all reasonable safety precautions and following the lab safety rules specific to the lab you are working in. In addition, you are responsible for reporting all safety issues to the laboratory supervisor, GTA or faculty responsible.

9 University Statements

9.1 Email Communication

As per university regulations, all students are required to check their e-mail account regularly: e-mail is the official route of communication between the University and its students.

9.2 When You Cannot Meet a Course Requirement

When you find yourself unable to meet an in-course requirement because of illness or compassionate reasons please advise the course instructor (or designated person, such as a teaching assistant) in writing, with your name, id#, and e-mail contact. The grounds for Academic Consideration are detailed in the Undergraduate and Graduate Calendars.

Undergraduate Calendar - Academic Consideration and Appeals

<https://www.uoguelph.ca/registrar/calendars/undergraduate/current/c08/c08-ac.shtml>

Graduate Calendar - Grounds for Academic Consideration

<https://www.uoguelph.ca/registrar/calendars/graduate/current/genreg/index.shtml>

9.3 Drop Date

Courses that are one semester long must be dropped by the end of the fortieth class day; two-semester courses must be dropped by the last day of the add period in the second semester. The regulations and procedures for course registration are available in the

Undergraduate and Graduate Calendars.

Undergraduate Calendar - Dropping Courses

<https://www.uoguelph.ca/registrar/calendars/undergraduate/current/c08/c08-drop.shtml>

Graduate Calendar - Registration Changes

<https://www.uoguelph.ca/registrar/calendars/graduate/current/genreg/genreg-reg-regchg.shtml>

9.4 Copies of Out-of-class Assignments

Keep paper and/or other reliable back-up copies of all out-of-class assignments: you may be asked to resubmit work at any time.

9.5 Accessibility

The University promotes the full participation of students who experience disabilities in their academic programs. To that end, the provision of academic accommodation is a shared responsibility between the University and the student.

When accommodations are needed, the student is required to first register with Student Accessibility Services (SAS). Documentation to substantiate the existence of a disability is required; however, interim accommodations may be possible while that process is underway.

Accommodations are available for both permanent and temporary disabilities. It should be noted that common illnesses such as a cold or the flu do not constitute a disability.

Use of the SAS Exam Centre requires students to book their exams at least 7 days in advance and not later than the 40th Class Day.

More information can be found on the SAS website

<https://www.uoguelph.ca/sas>

9.6 Academic Integrity

The University of Guelph is committed to upholding the highest standards of academic integrity, and it is the responsibility of all members of the University community-faculty, staff, and students-to be aware of what constitutes academic misconduct and to do as much as possible to prevent academic offences from occurring. University of Guelph students have the responsibility of abiding by the University's policy on academic misconduct regardless of their location of study; faculty, staff, and students have the responsibility of supporting an environment that encourages academic integrity. Students need to remain aware that instructors have access to and the right to use electronic and other means of detection.

Please note: Whether or not a student intended to commit academic misconduct is not relevant for a finding of guilt. Hurried or careless submission of assignments does not excuse students from responsibility for verifying the academic integrity of their work before submitting it. Students who are in any doubt as to whether an action on their part could be construed as an academic offence should consult with a faculty member or faculty advisor.

Undergraduate Calendar - Academic Misconduct

<https://www.uoguelph.ca/registrar/calendars/undergraduate/current/c08/c08-amisconduct.shtml>

Graduate Calendar - Academic Misconduct

<https://www.uoguelph.ca/registrar/calendars/graduate/current/genreg/index.shtml>

9.7 Recording of Materials

Presentations that are made in relation to course work - including lectures - cannot be recorded or copied without the permission of the presenter, whether the instructor, a student, or guest lecturer. Material recorded with permission is restricted to use for that course unless further permission is granted.

9.8 Resources

The Academic Calendars are the source of information about the University of Guelph's procedures, policies, and regulations that apply to undergraduate, graduate, and diploma programs.

Academic Calendars

<https://www.uoguelph.ca/academics/calendars>
