ENGG*3640 Microcomputer Interfacing
Fall 2014

UNIVERSITY of GUELPH

School of Engineering

(Revision 0: September 4, 2014)

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1 INSTRUCTIONAL SUPPORT

1.1 Instructor

Instructor: Radu Muresan, Ph.D., P.Eng.
Office: RICH 2509, ext. 56730
Email: rmuresan@uoguelph.ca
Office hours: Fridays: 13:00 – 15:30. Or by appointment

1.2 Lab Technician

Technician: Nathaniel Groendyk
Office: THRN 2308, ext. 53729
Email: groendyk@uoguelph.ca

1.3 Teaching Assistants

GTA	Email	Office Hours
Kamal Kamal	kamalk@uoguelph.ca	TBA on CourseLink
Matthew Mayhew	mmayhew@uoguelph.ca	TBA on CourseLink

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2 LEARNING RESOURCES

2.1 Course Website

Course material, news, announcements, and grades will be regularly posted to the ENGG*3640 CourseLink site and on my personal course webpage. You are responsible for checking the sites regularly.

2.2 Required Resources

1. Han-Way Huang, *HCS12/9S12 An Introduction to Software and Hardware Interfacing*, 2nd Edition, Delmar Cengage Learning, 2010.
2. Radu Muresan, *ENGG3640 Microcomputer Interfacing Lecture Notes*, University of Guelph CourseLink, 2014 Edition.
3. Radu Muresan, *ENGG3640 Lab Manual*, University of Guelph CourseLink, 2014 Edition.

2.3 Recommended Resources

1. Fredrick M. Cady, *Software and Hardware Engineering, Assembly and C Programming for the Freescale HCS12 Microcontroller*, 2nd Edition, Oxford, 2008.
2. J. W. Valvano, *Embedded Microcomputer Systems, Real Time Interfacing*, 2nd Edition, Thompson, 2007.
3. Freescale, 9S12DT256 Reference Manual, 2005.
4. Freescale, K60 Sub-Family Reference Manual, June 2012.
5. ARM, ARM Cortex-M4 Processor, Technical Reference Manual, 2013.
6. ARM, Cortex-M4 Devices, Generic User Guide, 2010.
7. Peter Spasov, *Microcontroller Technology: The 68HC11 and 68HC12 (5th Edition)*, Prentice Hall, 2004.
8. Harold S. Stone, *Microcomputer Interfacing*, Addison Wesley, 1983.

2.4 Additional Resources

Lecture Information: All the lecture notes are posted on the ENGG*3640 CourseLink system (week #1 to week #12) under ENGG3640 LECTURES module.

Lab Information: The ENGG3640 Lab Manual is posted on the ENGG*3640 CourseLink system under the LABORATORY module.

Assignments: The assignments and the solutions for the assignments are posted on the ENGG*3640 Courselink system under the ASSIGNMENTS module.

Exams: Some solutions of previous midterm exams will be posted on the ENGG*3640 CourseLink system under the EXAM SOLUTIONS section. Also, after the midterm exam a complete solution of the exam with the marking scheme applied will be posted for your reference.

Miscellaneous Information: Other information related to Microcomputer Interfacing topics will be posted on the web page.

2.5 Communication & Email

Please use lectures, tutorials and lab help sessions as your main opportunity to ask questions about the course. Major announcements will be posted to the course website. **It is your responsibility to check the course website regularly.** As per university regulations, all students are required to check their <mail.uoguelph.ca> e-mail account regularly: e-mail is the official route of communication between the University and its student.

3 ASSESSMENT

3.1 Dates and Distribution

Labs: 50%
See Section 5.4 below for due dates

Midterm: 20%
Monday Oct 20th, 19:00 - 21:00.

Final Exam: 30%
Thursday Dec 11th, 8:30 - 10:30, Room TBA on WebAdvisor

Assignments: Not marked. Comments and feedback will be provided

3.2 Course Grading Policies

Missed Assessments: If you are unable to meet an in-course requirement due to medical, psychological, or compassionate reasons, please email the course instructor. See the undergraduate calendar for information on regulations and procedures for Academic Consideration: http://www.uoguelph.ca/registrar/calendars/undergraduate/current/c08/c08-ac.shtml

Accommodation of Religious Obligations: If you are unable to meet an in-course requirement due to religious obligations, please email the course instructor at the start of the semester to make alternate arrangements. See the undergraduate calendar for information on regulations and procedures for Academic Accommodation of Religious Obligations: http://www.uoguelph.ca/registrar/calendars/undergraduate/current/c08/c08-accomrelig.shtml

Passing grade: In order to pass the course, you must meet the following conditions:

1. Students must finalize and submit all the labs (Demo + Report) and obtain a passing grade of 50% or higher in the lab portion of the course. Also the students are required to have a good participation on the work evaluation form (+/- 10% variation max allowed) otherwise they will have to redo the lab individually in order to obtain the group mark. The work evaluation form can be downloaded from the Engg*3640 CourseLink system found under the LABORATORY module. If an overall grade of lower than 50% is obtained in any lab, the students need to arrange with the instructor and the teaching assistant to reschedule a new demo and report submission.
2. Obtain an averaged passing grade of 50% or higher for the midterm exam or the final exam.
3. If the course passing conditions 1 and 2 are not met then the final course grade will be the average of the exams out of 100% (the laboratory grades will not be considered).

Contesting marks: All laboratory and midterm exam marks must be contested within 2 day from the grade submission. Also the exams must be written in pen or ink for contest considerations.

Missed midterm tests: If you miss a test due to grounds for granting academic consideration or religious accommodation, you will need to arrange a makeup exam date with the instructor.

Lab Work: You must attend and complete all laboratories. If you miss a laboratory demo due to grounds for granting academic consideration or religious accommodation, arrangements must be made with the teaching assistant to complete a makeup lab demo.

Late Lab Reports: Late submissions of lab reports will be accepted only with the approval of the course instructor. However, penalties on late submissions will be applied. Applied penalties will be posted on Engg*3640 CourseLink system.

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4 AIMS, OBJECTIVES & GRADUATE ATTRIBUTES

4.1 Calendar Description

This course focuses on the subject of interfacing microcomputers to external equipment. Topics include peripheral devices, hardware interfaces, device driver software and real time programming. Advanced programming such as: debugging of embedded systems, data structures and subroutine calls, high-level system programming. Interrupts and resets, real time events, signal generation and timing measurements. Synchronous and asynchronous serial communication. Parallel I/O ports and synchronization techniques. I/O interfacing, microcomputer busses, memory interfacing and direct memory access (DMA). Data acquisition topics include signal conditioning analog to digital conversion and digital signal processing.

Prerequisite(s): ENGG*2410, ENGG*2450
Restriction(s): ENGG*4640

4.2 Course Aims

This course is an introductory course in microcomputer interfacing and applications for students in computer engineering, electrical engineering, system and computing engineering and mechatronics engineering programs. The main goals of the course are: (1) to provide a broad and systematic introduction to microprocessors and microcontrollers, (2) to introduce the general HCS12 and Kinetis K60 architectures and interfacing as an example of typical microcontroller organizations and interfacing, (3) present typical applications and protocols in microcomputer interfacing.

4.3 Learning Objectives

At the successful completion of this course, the student will have demonstrated the ability to:

1. Microcontroller interfacing and microcontroller internal architecture
2. Understand hardware and software development tools
3. Program in assembly language
4. Interrupts, DMA, polling
5. Parallel I/O
6. Serial communication interfaces such as UART, serial peripheral interface SPI, inter-integrated circuit (I^2C), BDLC, and CAN bus.
7. Other current communication interfaces.
8. Functionality of busses
9. Data acquisition systems
10. Implement and demonstrate embedded applications.
11. Work with sensors and actuators
12. Design simple interfacing applications

4.4 Graduate Attributes

Successfully completing this course will contribute to the following CEAB Graduate Attributes:

Graduate Attribute	Learning Objectives	Assessment
1. Knowledge Base for Engineering	1, 3, 4, 5, 6, 7, 8, 9	Exams, Labs,
2. Problem Analysis	10, 11, 12	Labs
3. Investigation	10, 11	Labs
4. Design	10, 11, 12	Labs
5. Use of Engineering Tools	2, 10, 11	Labs
6. Communication	10	Labs
7. Individual and Teamwork	10, 11, 12	Labs
8. Professionalism	-	-
9. Impact of Engineering on Society and the Environment	-	-
10. Ethics and Equity	-	-
11. Environment, Society, Business, & Project Management	-	-
12. Life-Long Learning	1, 2, 12	Exams, Labs

4.5 Instructor's Role and Responsibility to Students

The instructor's role is to develop and deliver course material in ways that facilitate learning for a variety of students. All lecture notes plus various exercises, examples and referenced resources will be made available to students on CourseLink system in the appropriate module. However, these are not intended to be stand-alone course notes. During lectures, the instructor will expand and explain the content of notes and provide in class solutions to problems that supplement posted notes. Scheduled classes and labs will be the principal venue to provide information and feedback for tests and labs.

4.6 Students' Learning Responsibilities

Students are expected to take advantage of the learning opportunities provided during lectures and labs. In addition students are encouraged to consult the instructor and the TA during the scheduled office hours or to contact the instructor or TA for any help needed. Students, especially those having difficulty with the course content, should also make use of other resources recommended by the instructor. Students who do (or may) fall behind due to illness, work, or extra-curricular activities are advised to keep the instructor informed. This will allow the instructor to recommend extra resources in a timely manner and/or provide consideration if appropriate.

4.7 Relationships with other Courses & Labs

Previous Courses:

ENGG*2410 (Digital Systems Design): Boolean algebra and truth tables. Design and synthesis of digital circuits. Synchronous and asynchronous behaviour of digital circuits. Hardware testing. UART, microcotroller CPU, ALU and data acquisition systems.

ENGG*2450 (Electric Circuits): Fundamentals of electric circuits analysis. Circuits elements. Operational amplifiers.

Follow-on Courses:

ENGG*4420 (Real-Time Systems Design): Real-time concepts from a systems and computing perspective. Real-time operating systems for embedded designs. Real-time computer control and interfacing.

ENGG*4560 (Embedded Systems Design): Systems-on-chip and embedded systems design. Embedded systems design tools.

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5 TEACHING AND LEARNING ACTIVITIES

5.1 Timetable

Lectures:			
Tuesday	Sec 01, 02, 03	10:00 – 11:20	MCKN 120
Thursday	Sec 01, 02, 03	10:00 – 11:20	MCKN 120
Laboratory:			
Wednesday	Sec 01	14:30 – 16:20	RICH 1532
Friday	Sec 02	8:30 – 10:20	RICH 1532
Friday	Sec 03	15:30 – 17:20	RICH 1532
Seminar:			
Monday	Sec 01, 02, 03	13:30 – 14:20	MACK 225
Friday	Sec 01, 02, 03	14:30 – 15:20	JTP 212

5.2 Lecture Schedule

Week	Lecture Topics	References	Learning Objectives
1	Introduction to Interfacing, HCS12 and Kinetis K60 Microcontrollers	Chapter 1	1, 2
2	Microcomputer Interfacing, Assembly Language Programming. Hardware and Software Tools	Chapter 2, 3	1, 3
3	Assembly Language Design and Structure	Chapters 4, 6	3
4	Parallel Input/Output Interfacing	Chapters 4, 7	1, 5
5	General Interfacing Technique	Chapter 6	4
6	Timing Generation and Measurement	Chapter 8	1, 4
7	Data Acquisition System Part A	Chapter 12	1, 7, 9
8	Data Acquisition System Part B	Chapter 12	1, 7, 9
9	Serial Communication Part A	Chapter 9, 10	1, 6
10	Serial Communication Part B and Busses	Chapter 11, Lecture Notes	1, 6
11	Control Area Network (CAN)	Chapter 13	1, 6
19	Memory Interfacing. Direct Memory Access	Chapter 14	1, 4, 8

5.3 Design Lab Schedule

Week	Activity	References	Learning Objectives
1	Lab 0 introduction/implementation	Lab Manual	2
2	Lab 0 demo		2, 3
2	Lab 1 introduction/implementation	Lab Manual	1, 3
3	Lab 1 demo		10
3	Lab 2 introduction/implementation	Lab Manual	3, 4
4	Lab 2 demo		10
4-5	Lab 3 introduction/implementation	Lab Manual	3, 4
6	Lab 3 demo		10
6-7	Lab 4 introduction/implementation	Lab Manual	3, 4, 6, 11
8	Lab 4 demo		10
8-9	Lab 5 introduction/implementation	Lab Manual	3, 4
10	Lab 5 demo		10
10-12	Lab 6 introduction/implementation	Lab Manual	3, 4, 9, 11
12	Lab 6 demo		10

5.4 Lab Schedule

Week	Topic	Due
1	Safety Training and introduction to lab equipment	
1-2	Lab 0: Freescale HCS12 Evaluation Board and Software	Week 2: Demo
2	Lab 1: Basic Assembly Language and Program Development	Week 3: Demo/Report
3	Lab 2: Parallel Port Interfaces	Week 5: Demo/Report
4-5	Lab 3: Interfaces for Dipswitch, Keypad, Pushbuttons and LCD	Week 6: Demo/Report
6-7	Lab 4: Servo Motor Control using HCS12 Timers	Week 8: Demo/Report
8-9	Lab 5: ARM Cortex-M4 Development System	Week 10: Demo/Report
10-12	Lab 6: Interfacing the Kinetis K60 Microcontroller	Week 12: Demo/Report

5.5 Seminar Schedule

Week	Seminar Topics	References	Learning Objectives
1	Review: Electrical Circuits Primer	Lecture Notes	1
2	Review: Digital Logic Design Primer Part1	Lecture Notes	1
3	Microcomputer Interfacing HCS12 Programming	Assignment 1, 2, 3Handouts	1, 2, 3
4	General Interfacing Concepts	Assignment 4 Handout	1
5	Interrupt Driven I/O	Assignment 5 Handout	4
6	Review: Digital Logic Design Primer Part 2	Lecture Notes	1
7	Data Acquisition	Assignment 6 Handout	9
8	Serial Communication	Assignment 7 Handout	6
9	Busses	Assignment 8 Handout	6
10	Control Area Network (CAN)	Assignment 9 Handout	6
11	Memory Interfacing	Assignment 10 Handout	1
12	Course Review	Lecture Notes	

5.6 Other Important Dates

Thursday, September 4, 2014: First day of class
Monday, October 13, 2014 Holiday: no classes scheduled
Tuesday, October 14, 2014: Fall Study Break Day – no classes scheduled
Friday, October 31, 2014: 40th class day, last day to drop
Friday, November 28, 2014: Last day of class

You can refer the student undergraduate calendars for the semester scheduled dates.

6 LAB SAFETY

Safety is critically important to the School and is the responsibility of all members of the School: faculty, staff and students. As a student in a lab course you are responsible for taking all reasonable safety precautions and following the lab safety rules specific to the lab you are working in. In addition, you are responsible for reporting all safety issues to the laboratory supervisor, GTA or faculty responsible.

If the laboratory rules are not followed, consequences will include removing student's access to the lab. If this results in lab work not being completed, the student will receive a grade of 0.

7 ACADEMIC MISCONDUCT

The University of Guelph is committed to upholding the highest standards of academic integrity and it is the responsibility of all members of the University community faculty, staff, and students to be aware of what constitutes academic misconduct and to do as much as possible to prevent academic offences from occurring. University of Guelph students have the responsibility of abiding by the University's policy on academic misconduct regardless of their location of study; faculty, staff and students have the responsibility of supporting an environment that discourages misconduct. Students need to remain aware that instructors have access to and the right to use electronic and other means of detection.

Please note: Whether or not a student intended to commit academic misconduct is not relevant for a finding of guilt. Hurried or careless submission of assignments does not excuse students from responsibility for verifying the academic integrity of their work before submitting it. Students who are in any doubt as to whether an action on their part could be construed as an academic offence should consult with a faculty member.

7.1 Resources

The Academic Misconduct Policy is detailed in the Undergraduate Calendar:
http://www.uoguelph.ca/registrar/calendars/undergraduate/current/c08/c08-amisconduct.shtml

A tutorial on Academic Misconduct produced by the Learning Commons can be found at:
http://www.academicintegrity.uoguelph.ca/

Please also review the section on Academic Misconduct in your Engineering Program Guide.

The School of Engineering has adopted a Code of Ethics that can be found at:
http://www.uoguelph.ca/engineering/undergrad-counselling-ethics

8 ACCESSIBILITY

The University of Guelph is committed to creating a barrier-free environment. Providing services for students is a shared responsibility among students, faculty and administrators. This relationship is based on respect of individual rights, the dignity of the individual and the University community's shared commitment to an open and supportive learning environment. Students requiring service or accommodation, whether due to an identified, ongoing disability for a short-term disability should contact the Centre for Students with Disabilities as soon as possible.

For more information, contact CSD at 519-824-4120 ext. 56208 or email csd@uoguelph.ca or see the website: http://www.csd.uoguelph.ca/csd/

9 RECORDING OF MATERIALS

Presentations which are made in relation to course work—including lectures—cannot be recorded or copied without the permission of the presenter, whether the instructor, classmate or guest lecturer. Material recorded with permission is restricted to use for that course unless further permission is granted.

10 RESOURCES

The Academic Calendars are the source of information about the University of Guelph's procedures, policies and regulations which apply to undergraduate, graduate and diploma programs: http://www.uoguelph.ca/registrar/calendars/index.cfm?index